

TOSHIBA Microcontrollers TLCS-870 Family
TLCS-870/C Series

TMP86FM26

Dear Customer

March 2006

Restriction on Use of the Interrupt Controller

This is to inform you of a restriction that has been found with regard to the interrupt controller contained in some of the TLCS-870/C Series microcontrollers. If you need further information, please contact your local Toshiba sales office.

[Phenomenon]

The interrupt latch IL15 (INTSIO: SIO interrupt) may be cleared without the interrupt that set the latch being processed.

[Condition]

If an INTSIO interrupt request is generated while an interrupt with priority lower than IL15 (INTSIO: SIO interrupt) is being accepted, the interrupt latch IL15 is cleared immediately after it has been set.

[Restriction]

Because of the phenomenon described above, it is prohibited to use the interrupt source INTSIO (SIO interrupt) together with interrupts with priority lower than IL15 (i.e., IL16, IL17, IL18, ...) in the TMP86FM26.

When using the SIO interrupt, please be sure to disable interrupts IL16 and later. We apologize for any inconvenience this may cause.